

Notice of References Cited

Application/Control No.

09/866,800

Applicant(s)/Patent Under
Reexamination
OKAWA ET AL.

Examiner

Johannes P Mondt

Art Unit

2826

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	C	US-			
	D	US-			
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	K	US-			
	L	US-			
	M	US-			

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	S. Wolf, "Silicon Processing for the VLSI Era", Volume 3 - The Submicron MOSFET, pages 523-527, Lattice Press, Sunset Beach, California (USA), 1995 edition (ISBN 0-961672-5-3).
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.